

Revisions				216323	C
LAL	Rev.	Description	Chk	Date	Approved
	A	ENGRG RELEASE	<i>RF</i>	3-4-76	<i>RF</i>
	B	REVISED	<i>RF</i>	6-16-76	<i>RF</i>
	C	M/I. ITEM 45 WAS 10K P/N 116447-103. SH 9 RI WAS 10K.	<i>RF</i>	9-28-76	<i>RF</i>

ENGINEERING  
RELEASE

THIS DOCUMENT CONTAINS 23 SHEETS.

Dist Code ALT

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		1. Tolerances .XX ±.03      Angular .XXX ±.010    ±½° 2. Break All Sharp Edges .010 Approx. 3. Mach. Surfaces <input checked="" type="checkbox"/> 4. All Dim. in Inches		Check <i>3-3-76</i>	Title		
				Appr. <i>3-3-76</i>	ALTO II		
				Material	ASSEMBLY, PRINTED WIRING- ETHERNET		
Model No. First Use	ALTO II EW07YF92	Finish		Code Ident.	Size	Dwg. No.	Change Letter
				18338	A	216323	C
Next Assy. First Use	216302			Scale	Do Not Scale Drawing	Sheet	1

NOTES: UNLESS OTHERWISE SPECIFIED

1. THE FOLLOWING MODIFICATIONS HAVE TO BE MADE TO THIS ASSEMBLY USING "B" REVISION PRINTED WIRING BOARDS:

CUT ETCH BETWEEN

- 1) A47-12 AND A50-2
- 2) A50-2 AND A60-5
- 3) A50-13 AND A54-6

ADD WIRE FROM

- 1) A47-4 TO A60-5
- 2) A54-6 TO A60-5
- 3) A50-2 TO A64-6
- 4) A64-4 TO A77-7
- 5) A64-5 TO A75-6
- 6) A50-13 TO EC PIN 76

2. ASSEMBLE PER ALTO II MODULE ASSEMBLY SPECIFICATION, DWG NO. 216207.

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Title  
ASSY, PRINTED WIRING  
ETHERNET

Xerox Corporation  
El Segundo, California

**XEROX**

216323

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Sheet 2 OF

ASSY NO. 216323

PWB NO. 216325

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**Material List**

ML	Drawing No. 216323	Rev. C
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Rev.	ALTO II	Model No. EWO 7YF92	Date 3-3-76	Sheet 4 of	
Drawing No. 216323	ASSEMBLY, PRINTED WIRING-- ETHERNET				
Item No.	Drawing Title	Drawing No.	No. Req.	Remarks	
1	Board, P.W. Ethernet	216325	1		
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3	Spec, Module Assembly	216207	REF		
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<b>ML</b>	Drawing No. <b>216323</b>	Rev. <b>C</b>
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Model No.

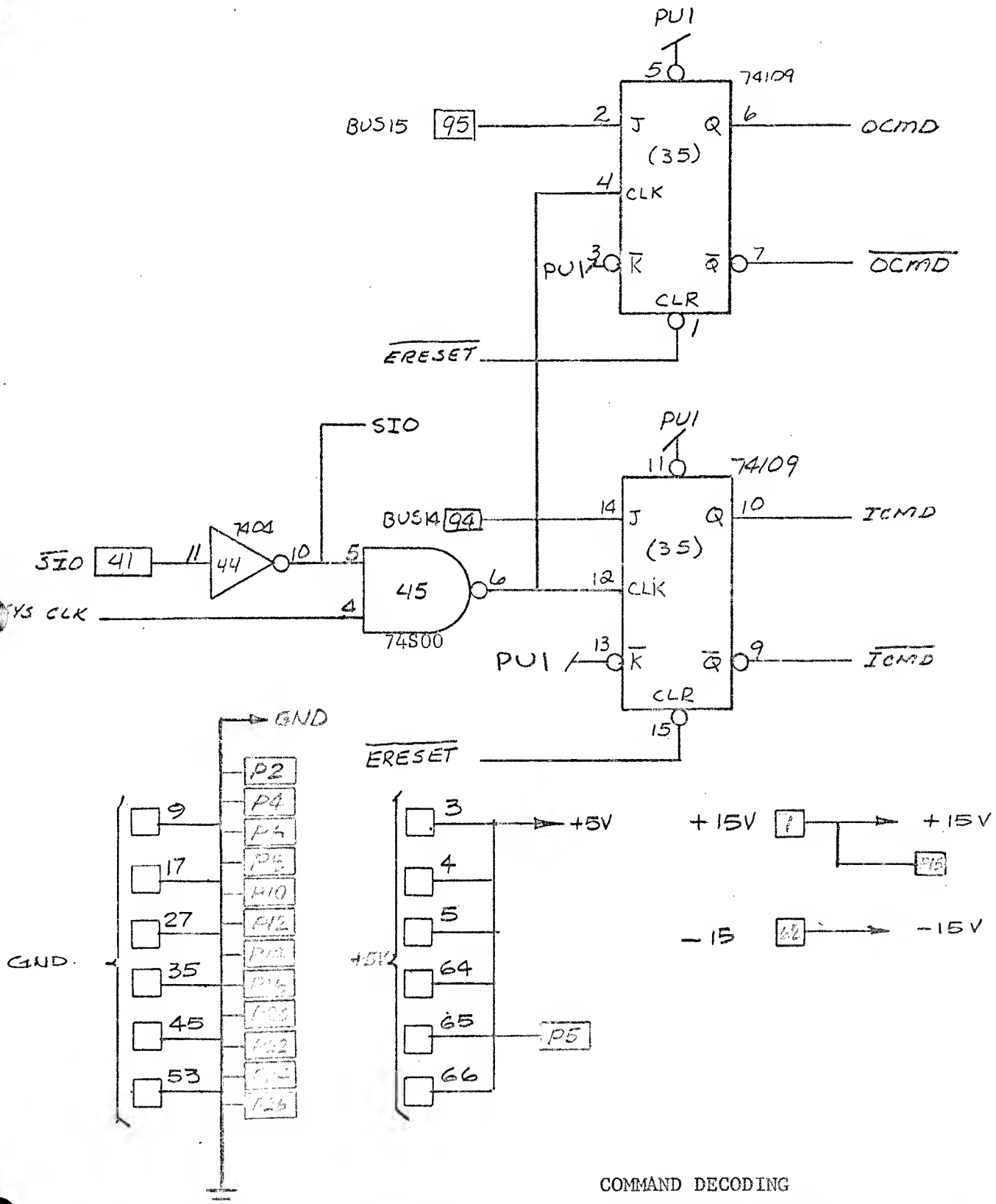
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COMMAND DECODING

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Title ALTO II  
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ETHERNET

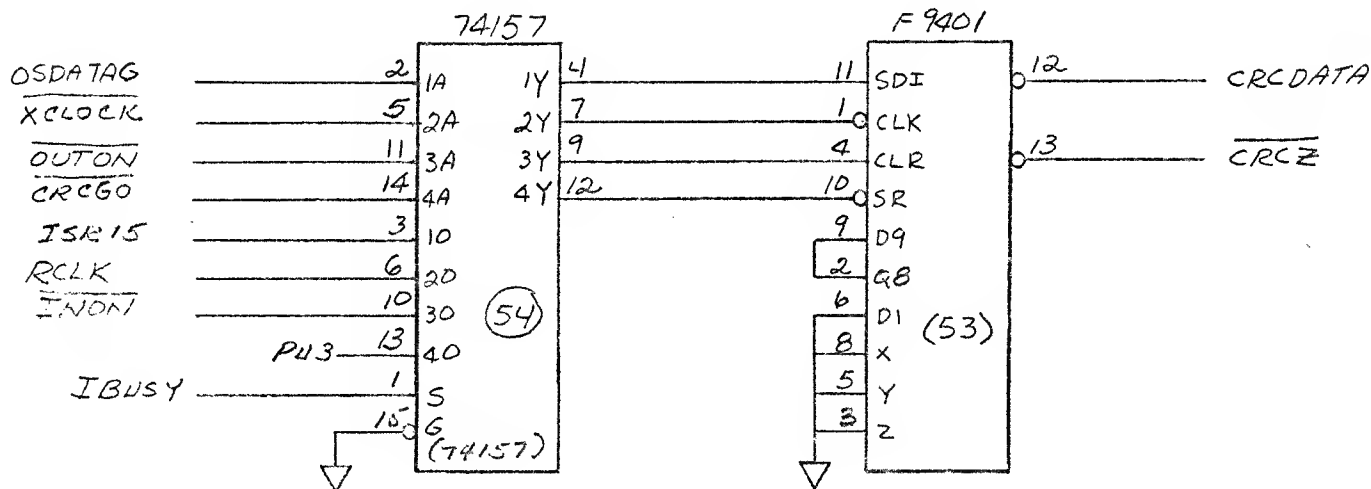
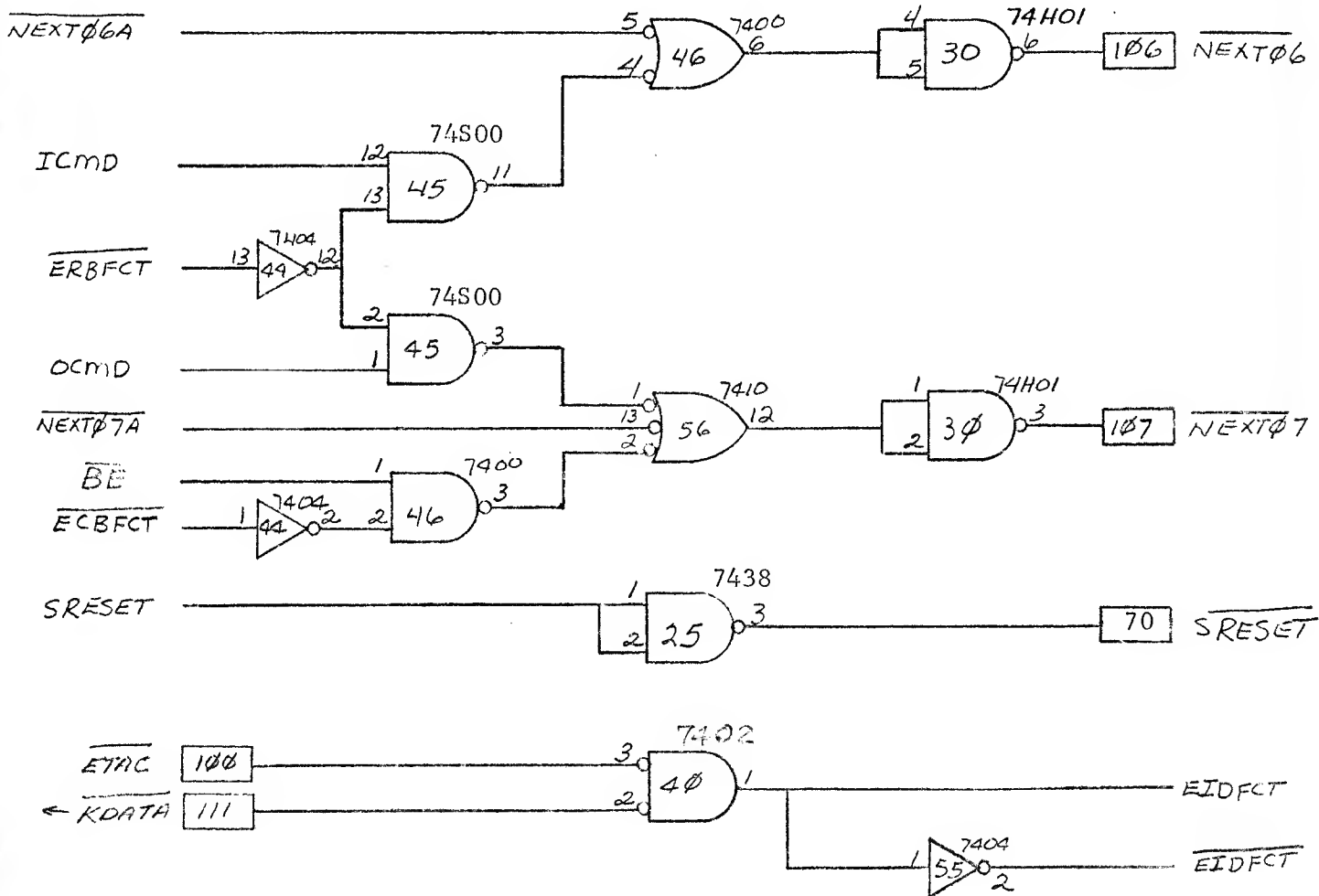
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# BRANCH LOGIC

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Title  
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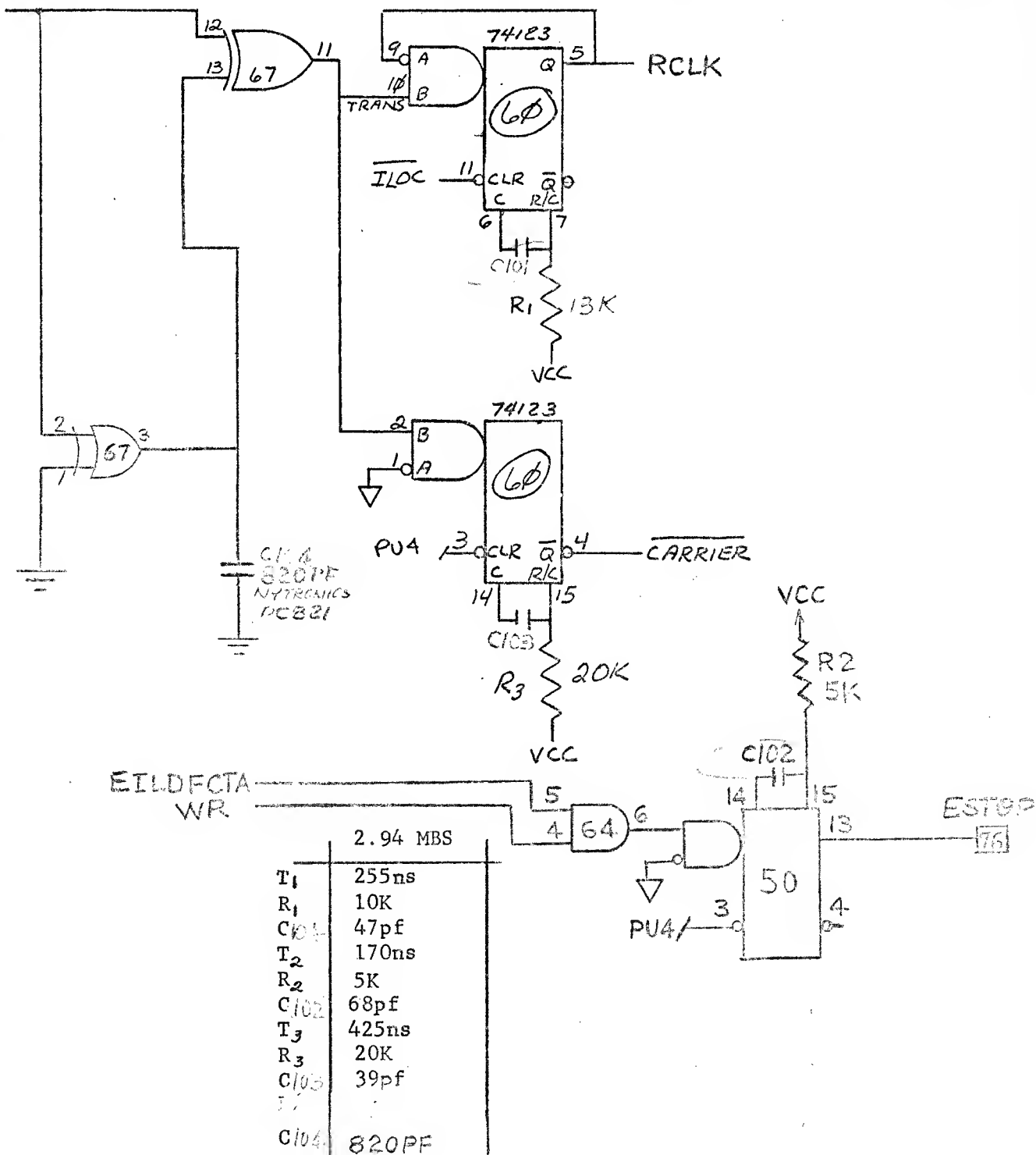
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RDATA



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Title

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ETHERNET

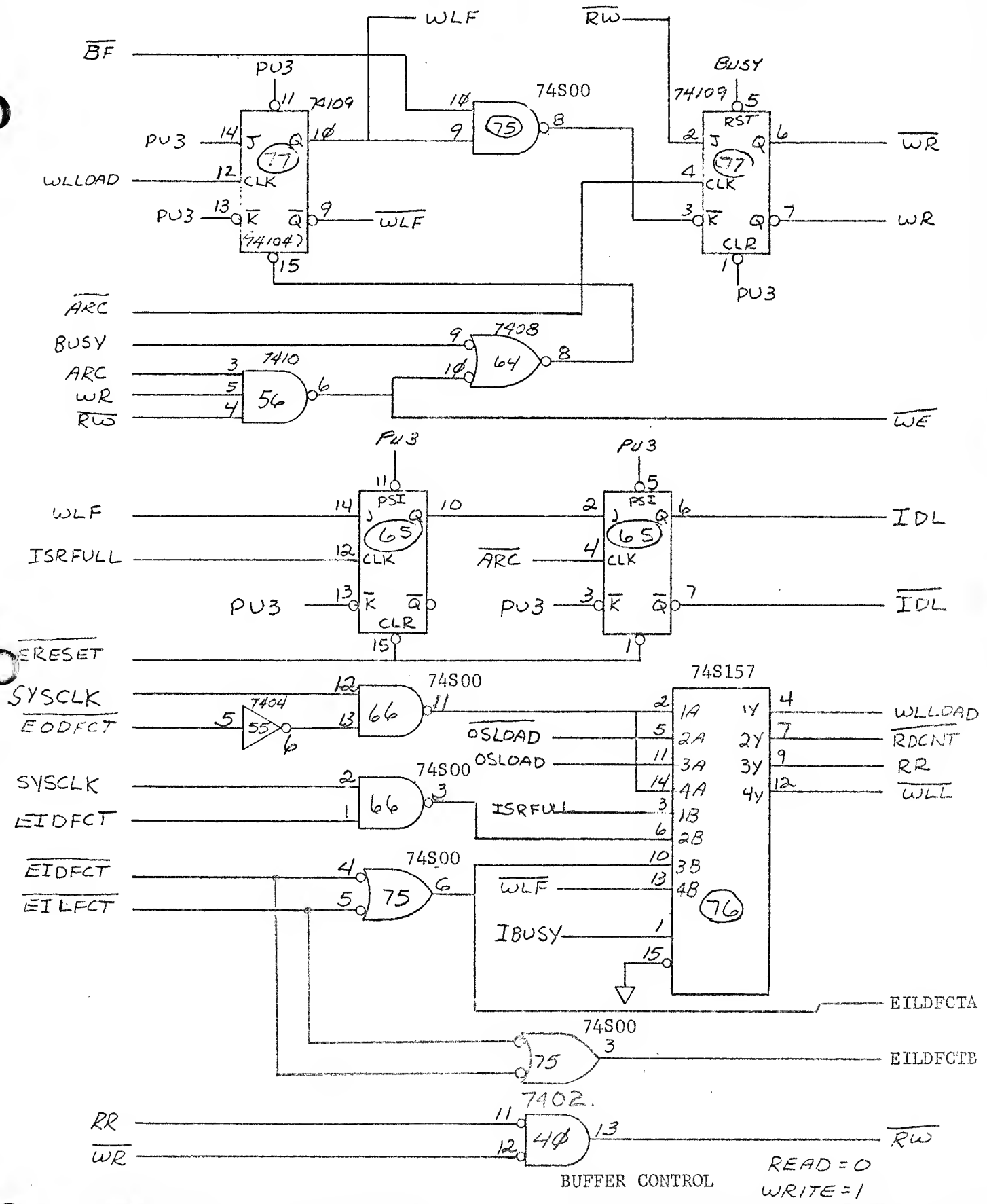
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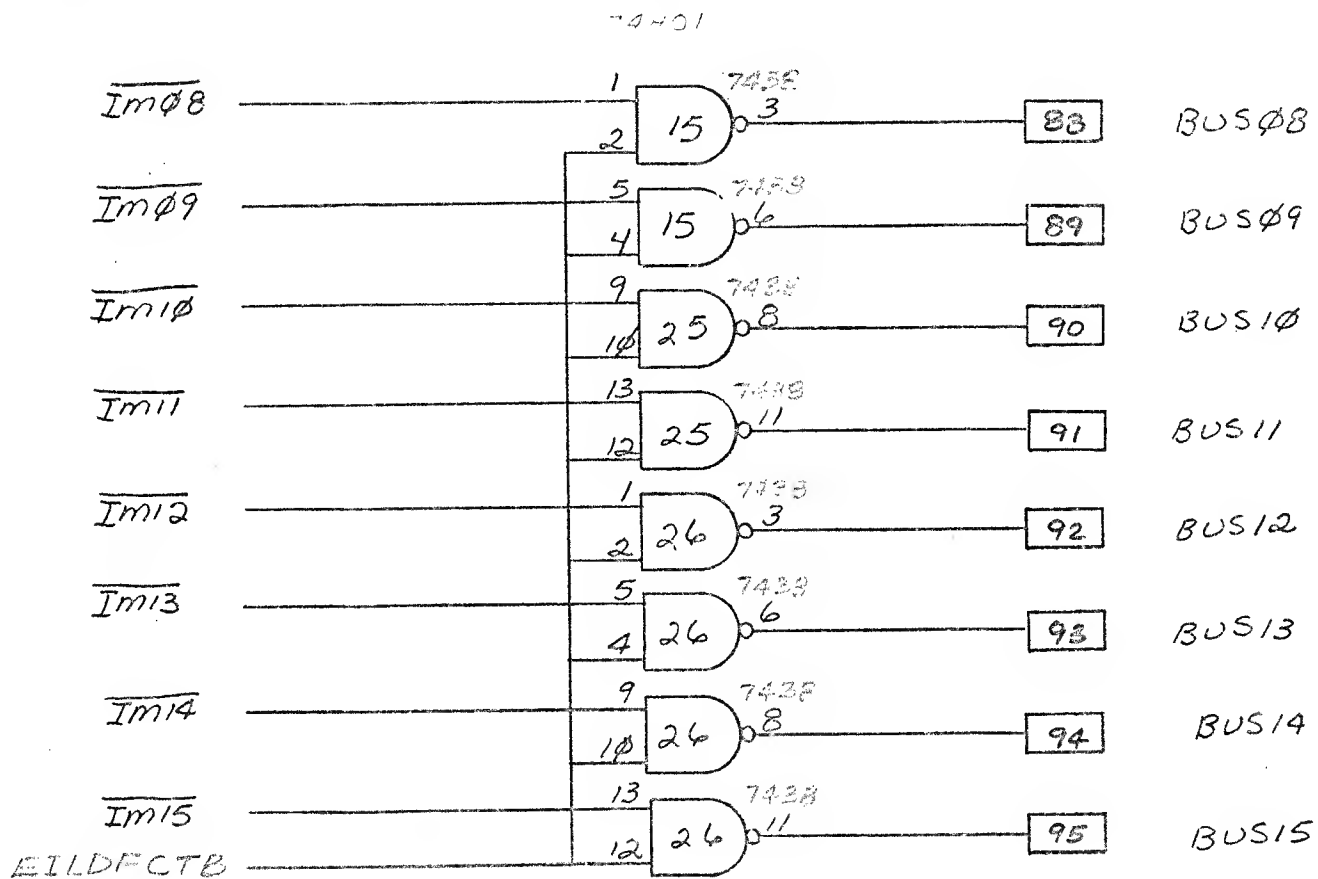
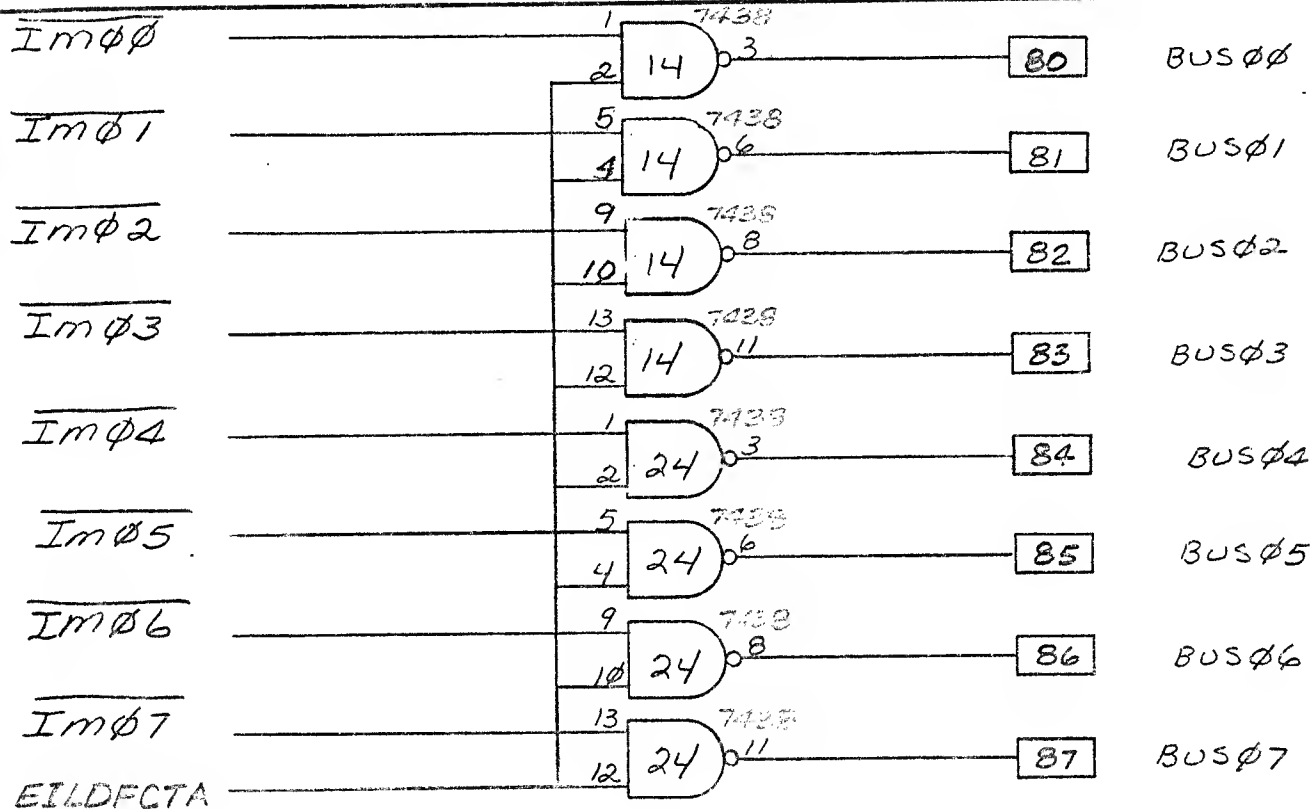
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# PROCESSOR BUS DRIVERS

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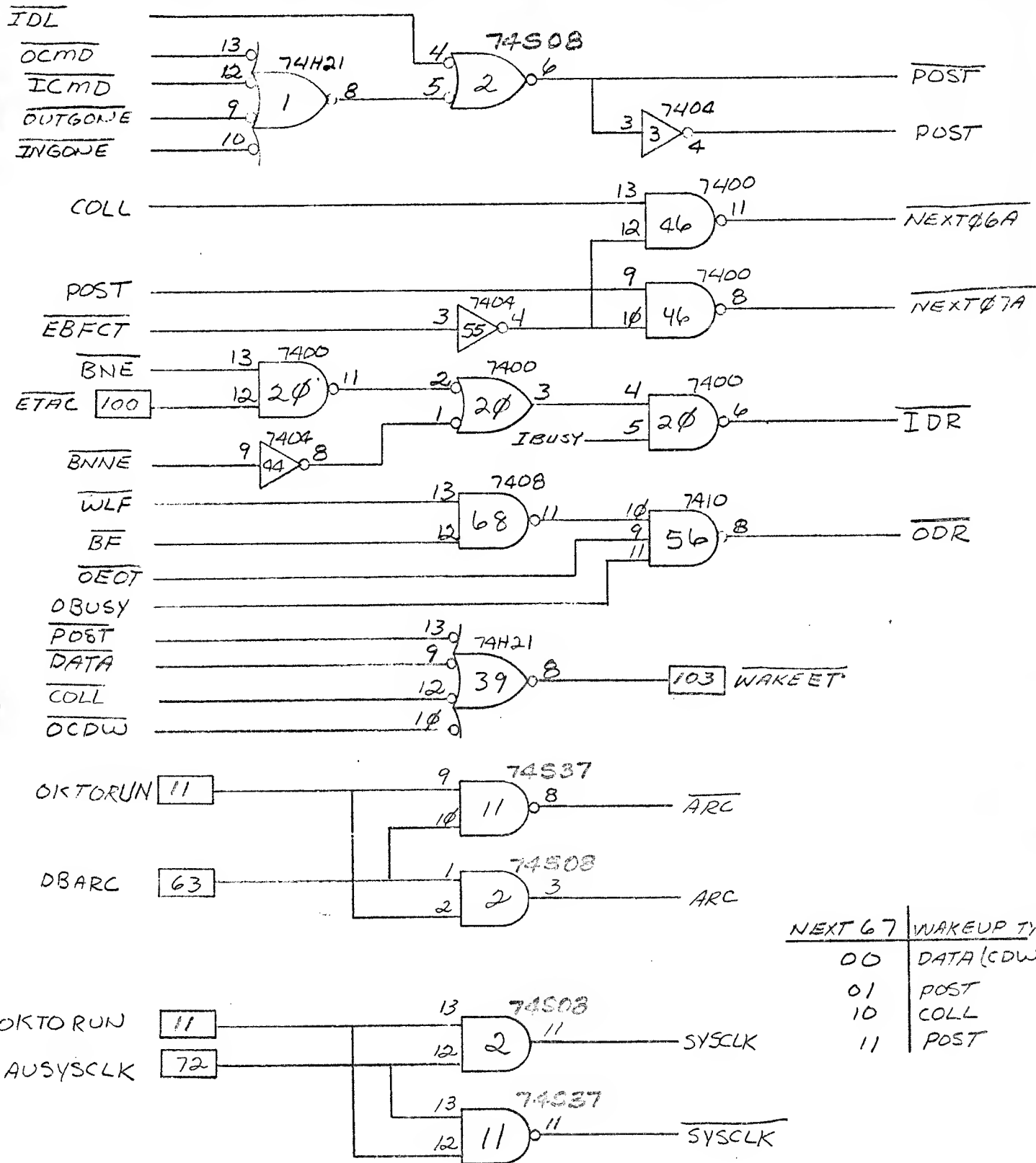
Title  
ALTO II  
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ETHERNET

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NEXT 67	WAKEUP TYPE
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01	POST
10	COLL
11	POST

# ALTO INTERFACE

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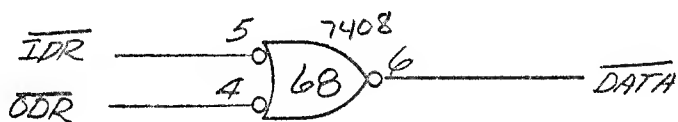
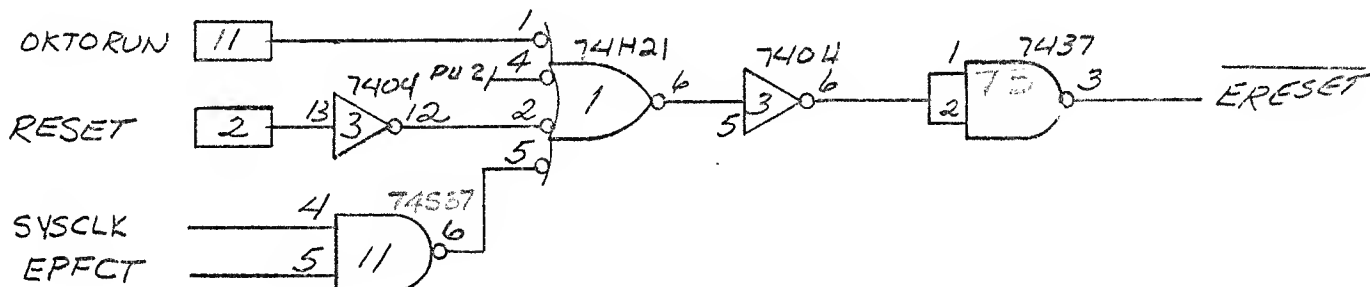
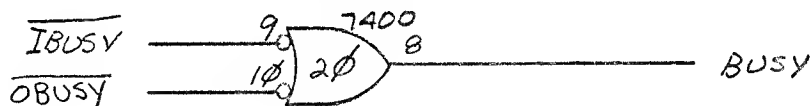
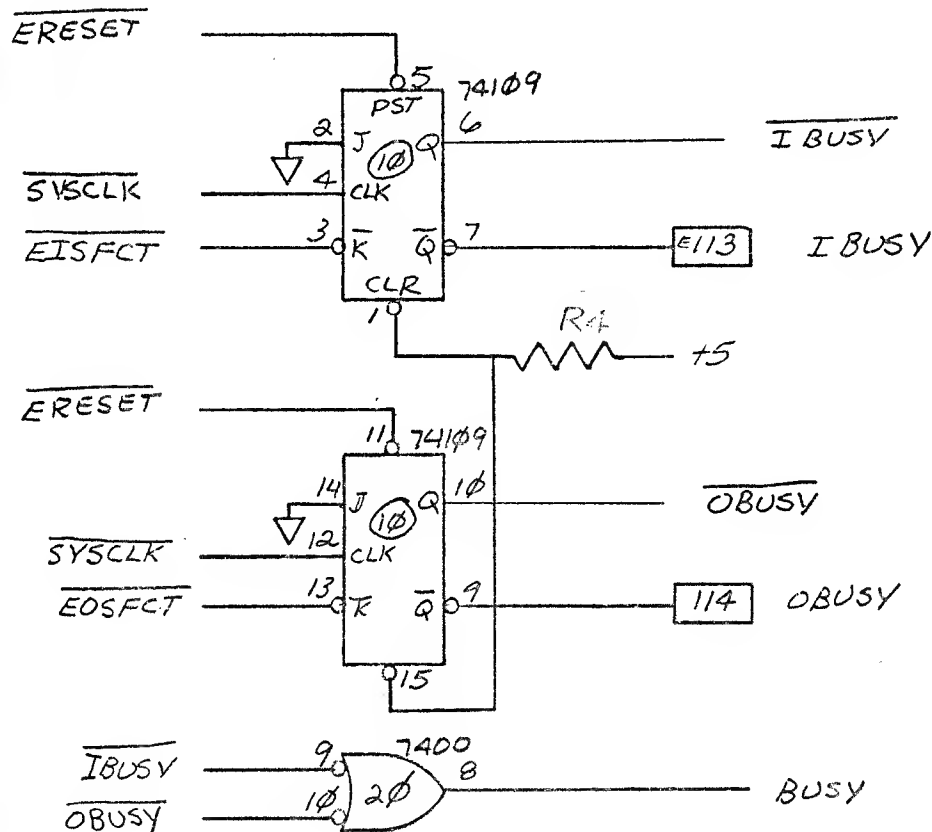
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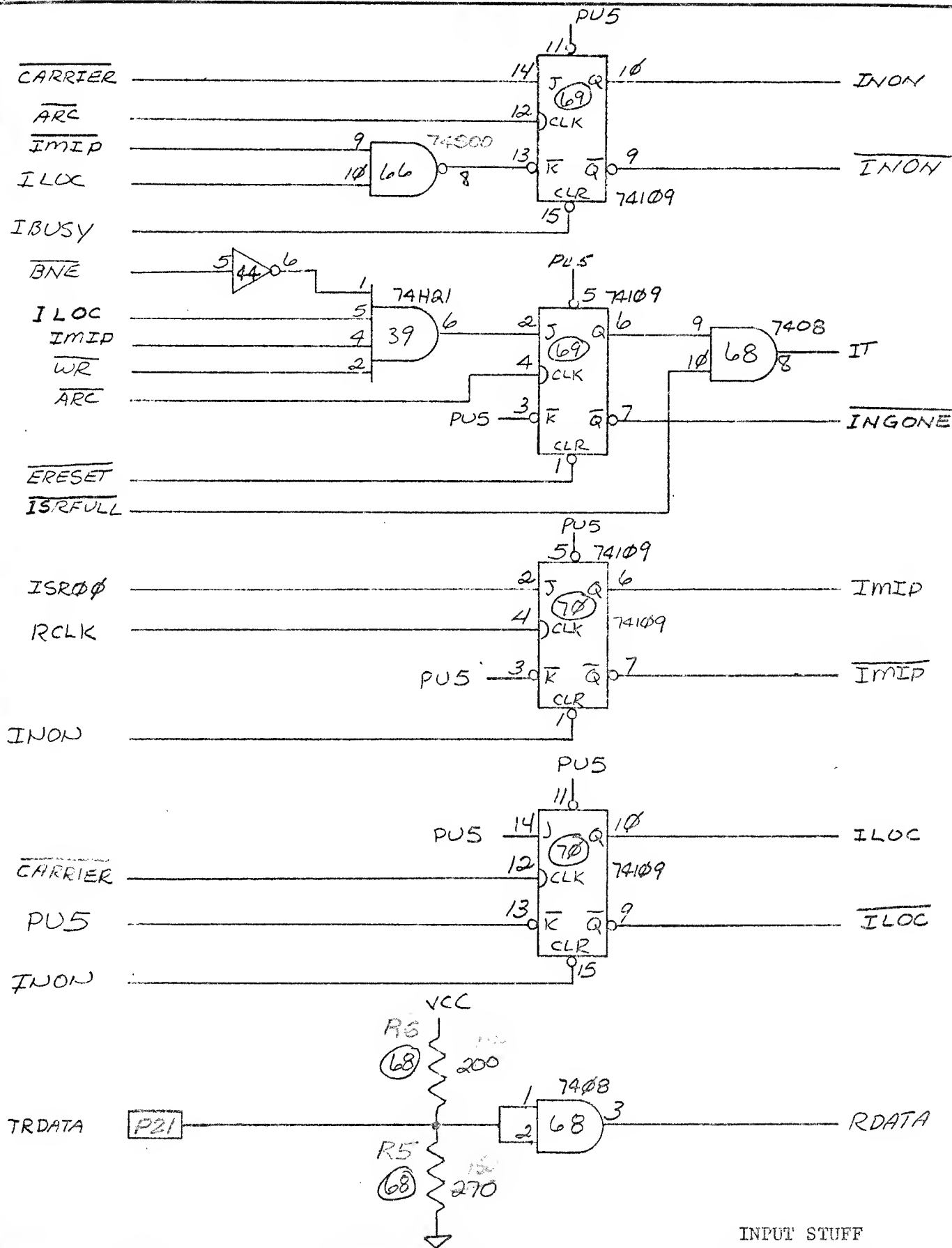
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INPUT STUFF

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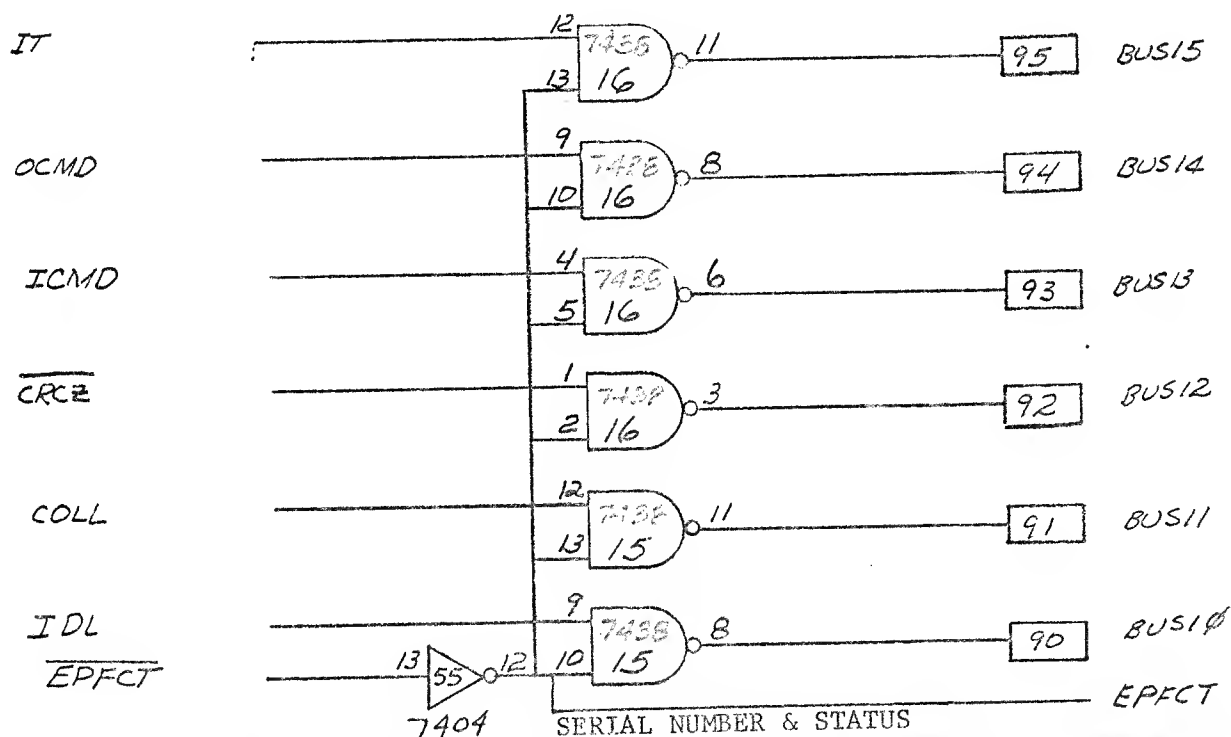
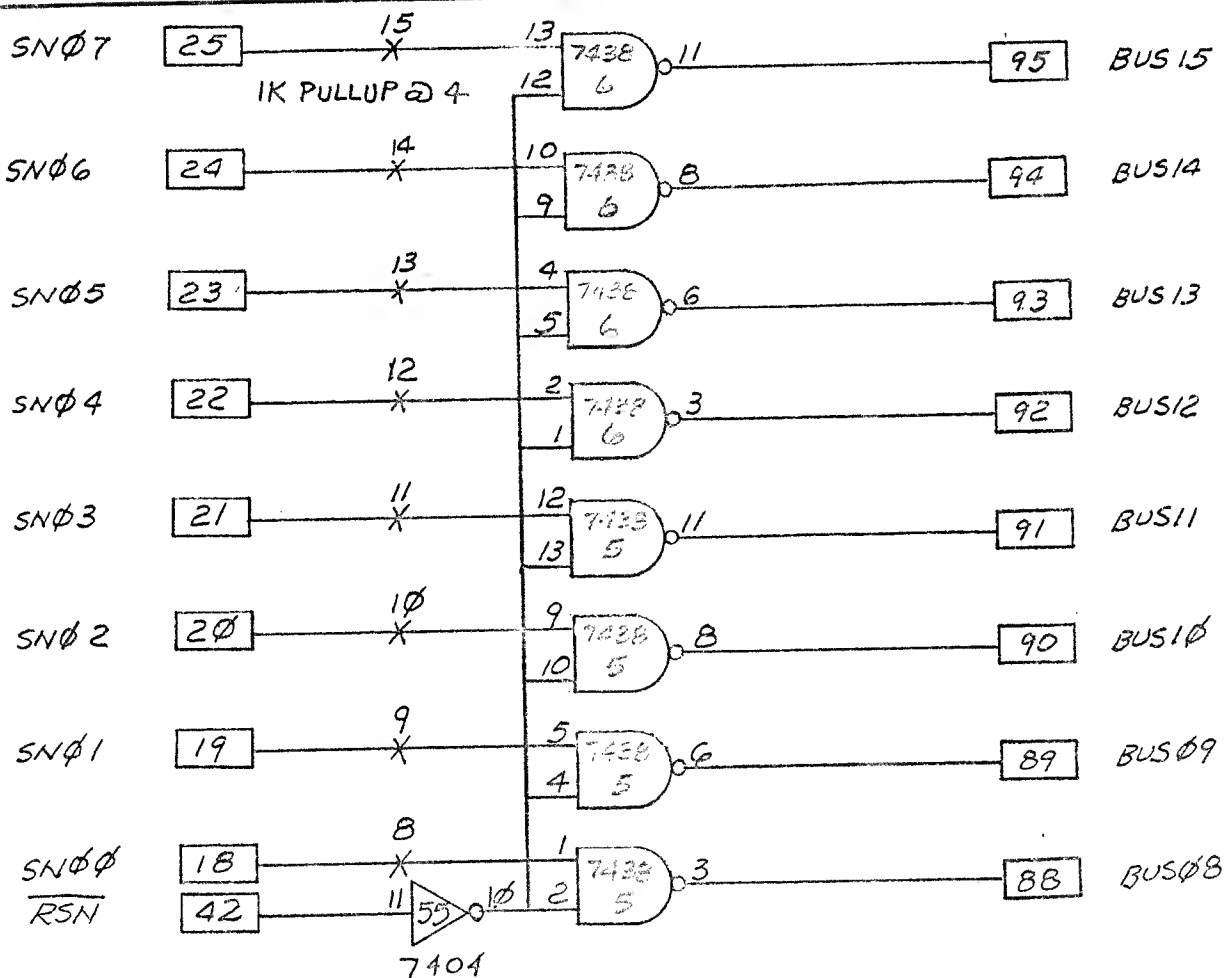
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SERIAL NUMBER & STATUS

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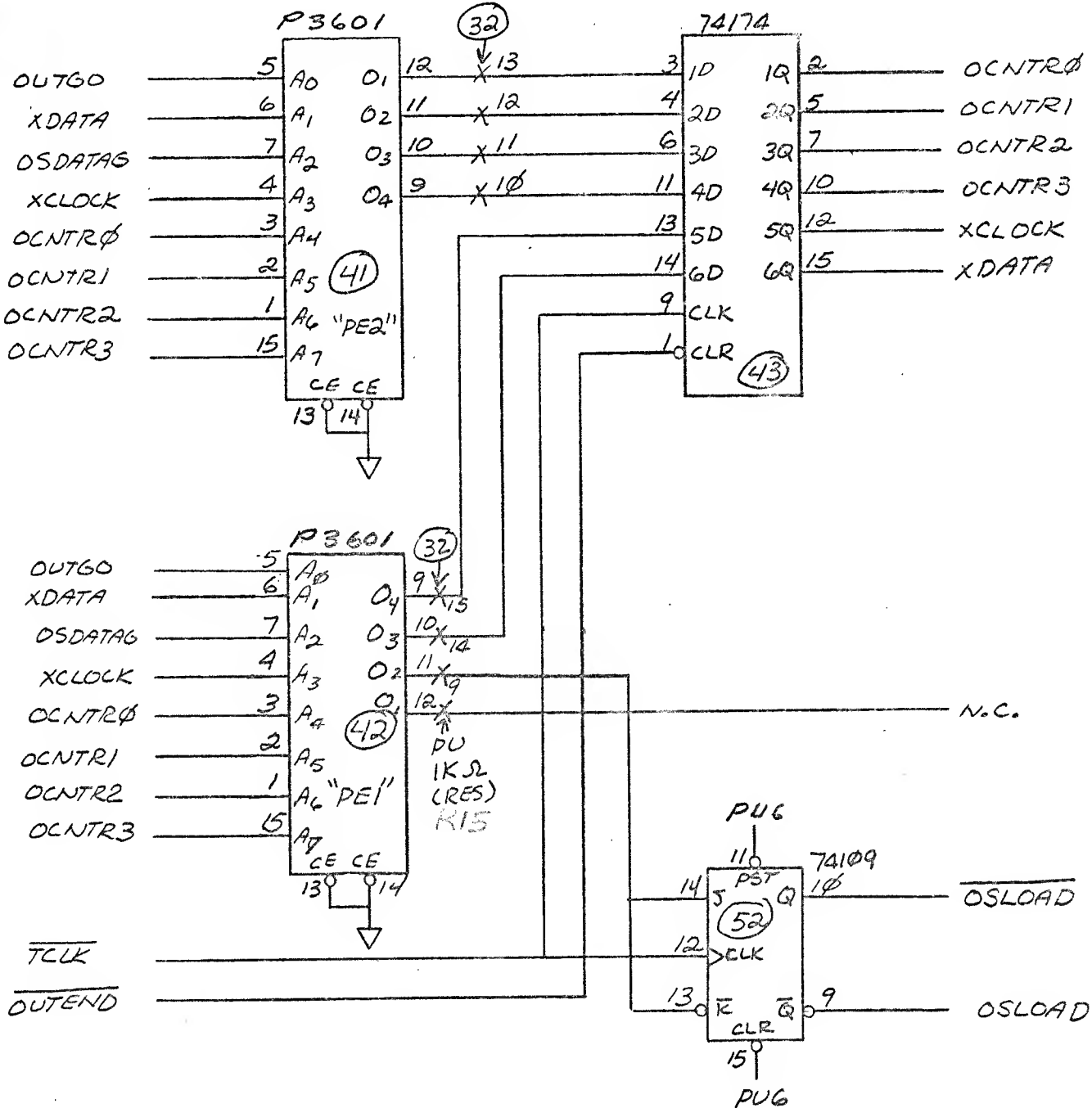
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OUTPUT S.R. & PHASE ENCODER

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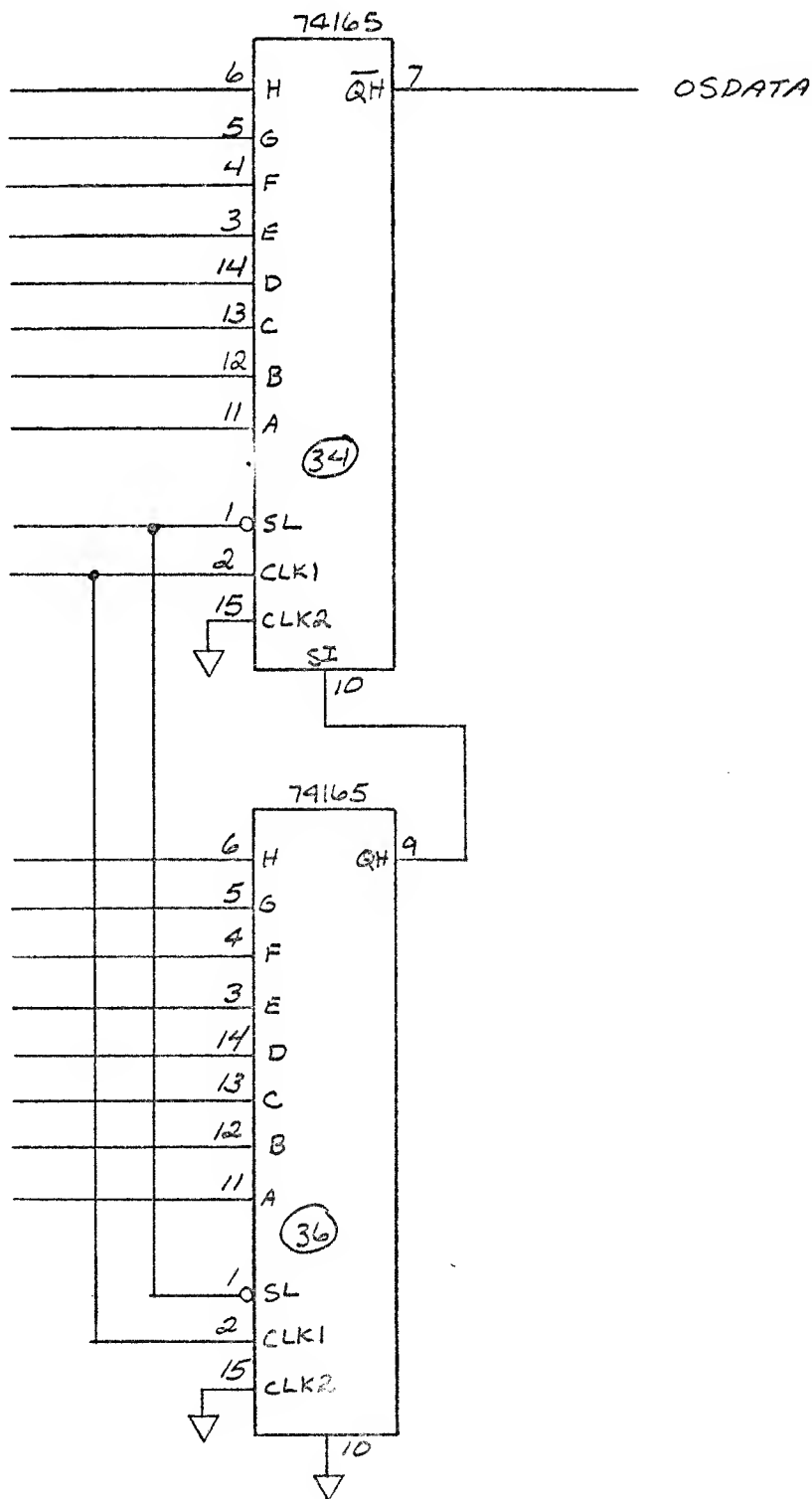
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IM00  
IM01  
IM02  
IM03  
IM04  
IM05  
IM06  
IM07

OSLOAD  
XCLOCK

IM08  
IM09  
IM10  
IM11  
IM12  
IM13  
IM14  
IM15



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COLL

XDATA

$\overline{COLL}$

TROTHER

DBARC

OK TORUN

XDATA

OTHER

TCLK

$\overline{TCLK}$

OUTPUT STUFF

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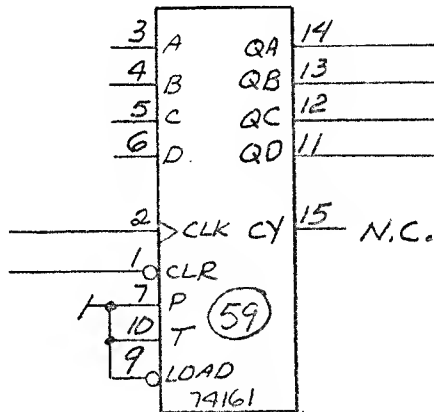
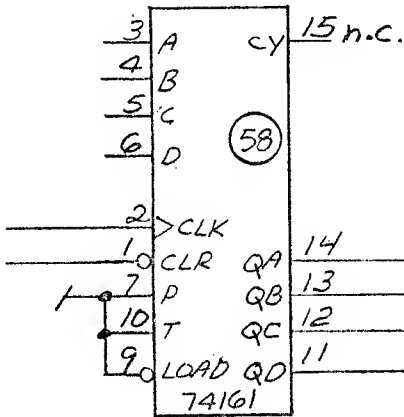
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BUSY  
PU4

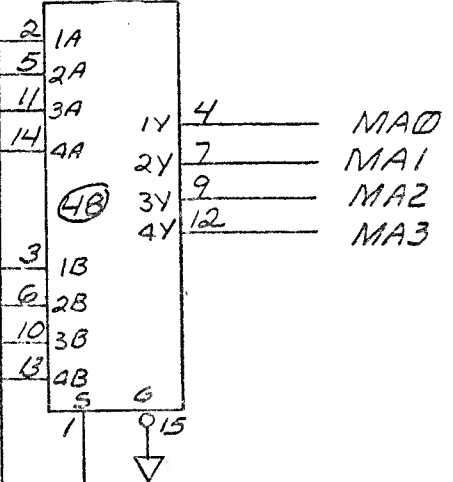
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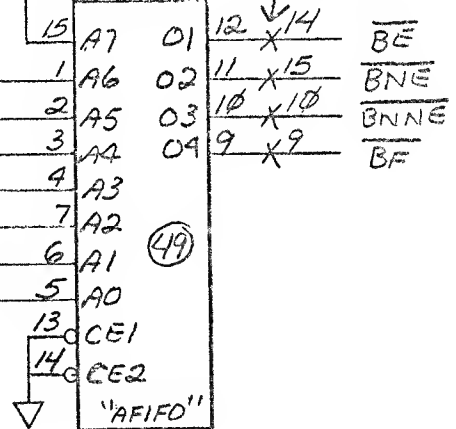
RW



74157



P3601



HALF DUPLEX BUFFER

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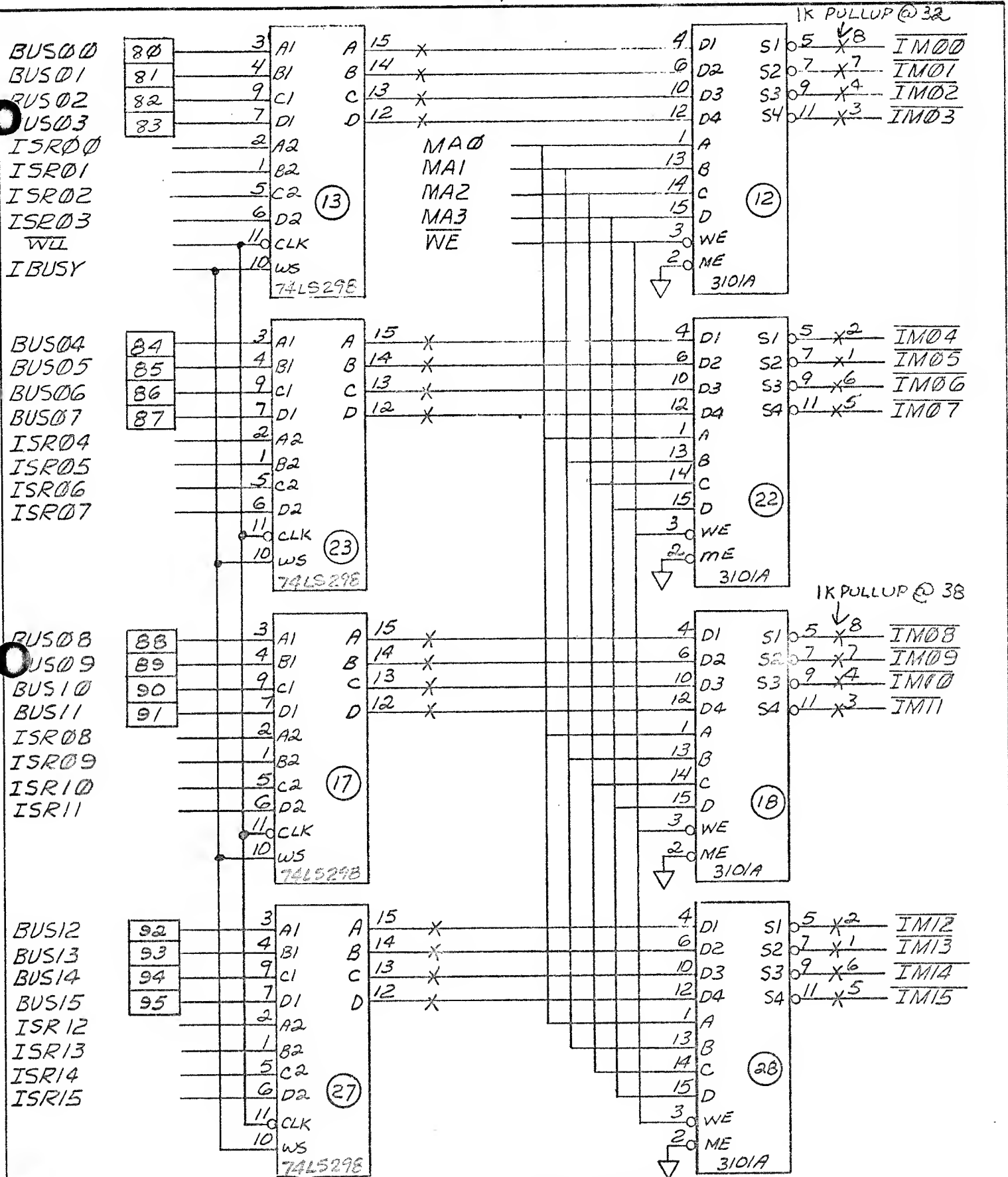
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